



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

INFORMATION

**DISCLOSURE** <u>STATEMENT</u>

In Re PATENT APPLICATION Of:

Applicant:

Yoshinori SHIZUNO

Serial No.:

10/697,315

Filed:

October 31, 2003

For:

SEMICONDUCTOR DEVICE WITH

SIMPLIFIED CONSTITUTION

Atty Ref.:

OHG 141

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an Information Disclosure Statement submitted in compliance with the timing requirements of 37 C.F.R. § 1.97(b)(3), i.e., prior to a First Office Action on the merits.

Attached are copies of one Japanese patent publication and two Japaneselanguage articles, which were cited in related co-pending application No. 10/697,156, from which any relevance of the documents may be gleaned. Any further relevance of the publication can be gleaned from the attached Englishlanguage Abstract. Any further relevance of the Japanese-language articles can be gleaned from pages 1 and 2 of the related co-pending application No. 10/697,156, where these articles are discussed. The publications and articles are listed on the attached Form PTO-1449. Also listed on the Form PTO-1449 for consideration is one U.S. patent, which was also cited in related co-pending application No. 10/697, 156. Any relevance of this U.S. patent is self-evident.



Atty Dkt: OHG 141/RHB

Since this Information Disclosure Statement is being filed before a first office action on the merits, no fee or certification is required, and the requirements of 37 C.F.R. §§ 1.97 and 1.98 are deemed to be fully met as to all listed documents.

Consideration of the listed documents is respectfully requested.

Should any fees be required, please charge the same to our deposit account No. 18-0002, and notify us accordingly.

October 27, 2004

Date

Respectfully submitted,

Robert H. Berdo, Jr. (Reg. No. 38, 075)

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RHB:vm

FORM PTO-1449  INFORMATION DISCLOSURE STATEMENT  OFF  OCT 2 7 2004				Atty Docket	Applic	Application No.		
				OHG 141	10/697	10/697,315		
				Applicant Yoshinori SHIZUNO				
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FRADENANCE				October 31, 2003	To Be	To Be Assigned		
			U.S.	PATENT DOCUMENTS				
Examiner Initial		Document Number	Date	Name	Class	Sub- Class	Filing Date	
	AA	6,455,920	09/24/02	Fukasawa et al.				
	AB							
	AC							
	AD							
	AE					. <u>.</u>		
	AF					_		
	AG							
	AH							
	·-1		FOREIG	GN PATENT DOCUMENTS				
		Document Number	Date	Country	Class	Sub- Class	Trans- lation	
	Al	11251493A	09/17/99	Japan			Abstrac	
	AJ							
	AK							
	AL							
		ОТНЕ	ER (Including	Author, Title, Date, Pertinent Page	s, etc.)			
	AM	Nikkei Microdevices, February, 1999, p48-57						
<del></del>	AN	Nikkei Microdevices, February, 2000, p50-53						
	AO							
	AP							
xaminer				Date Consi	dered			
XAMINER	: Initial	if reference cons	sidered, whether	er or not citation is in conformance	with MPEP 609;	draw line	through	